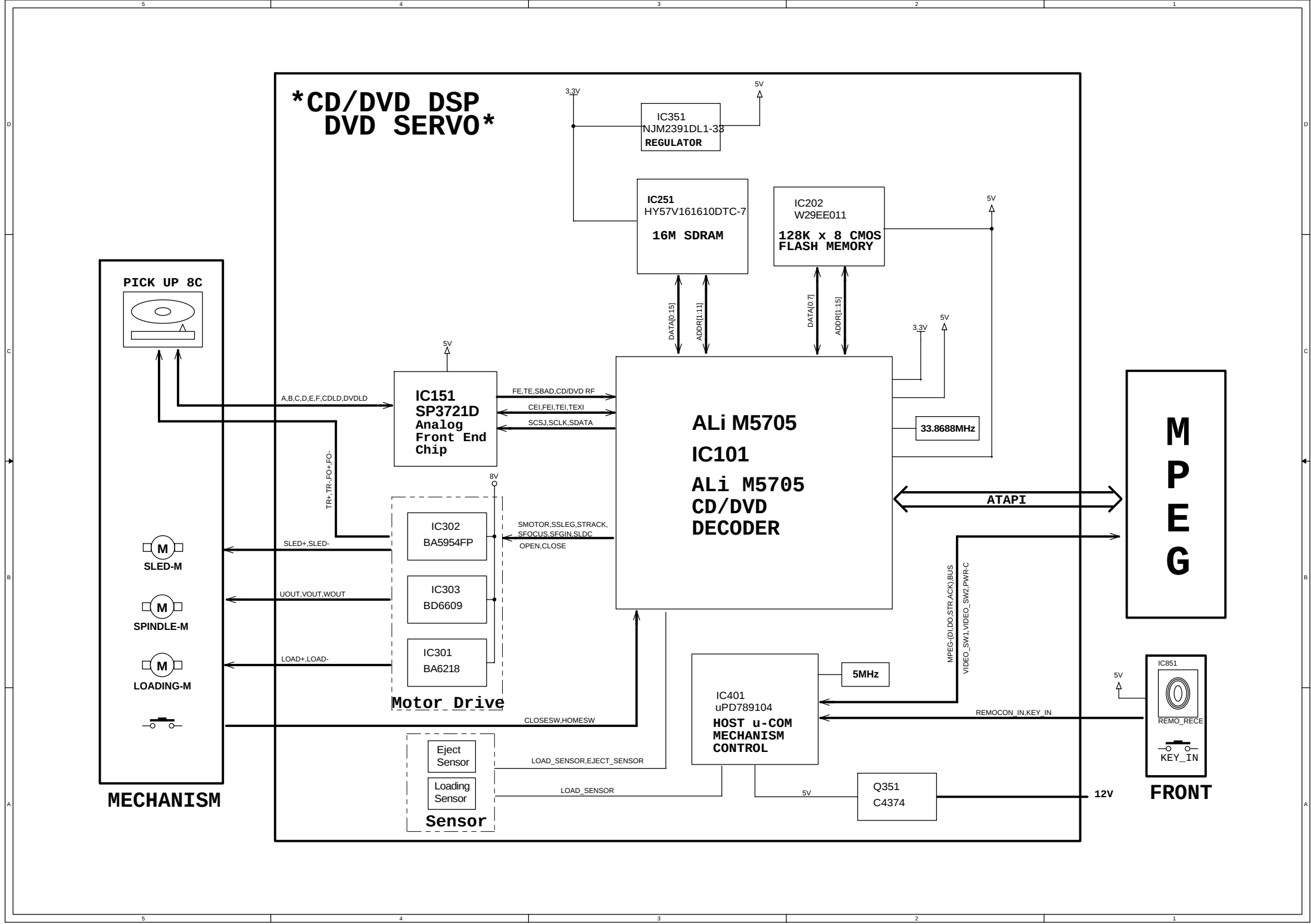
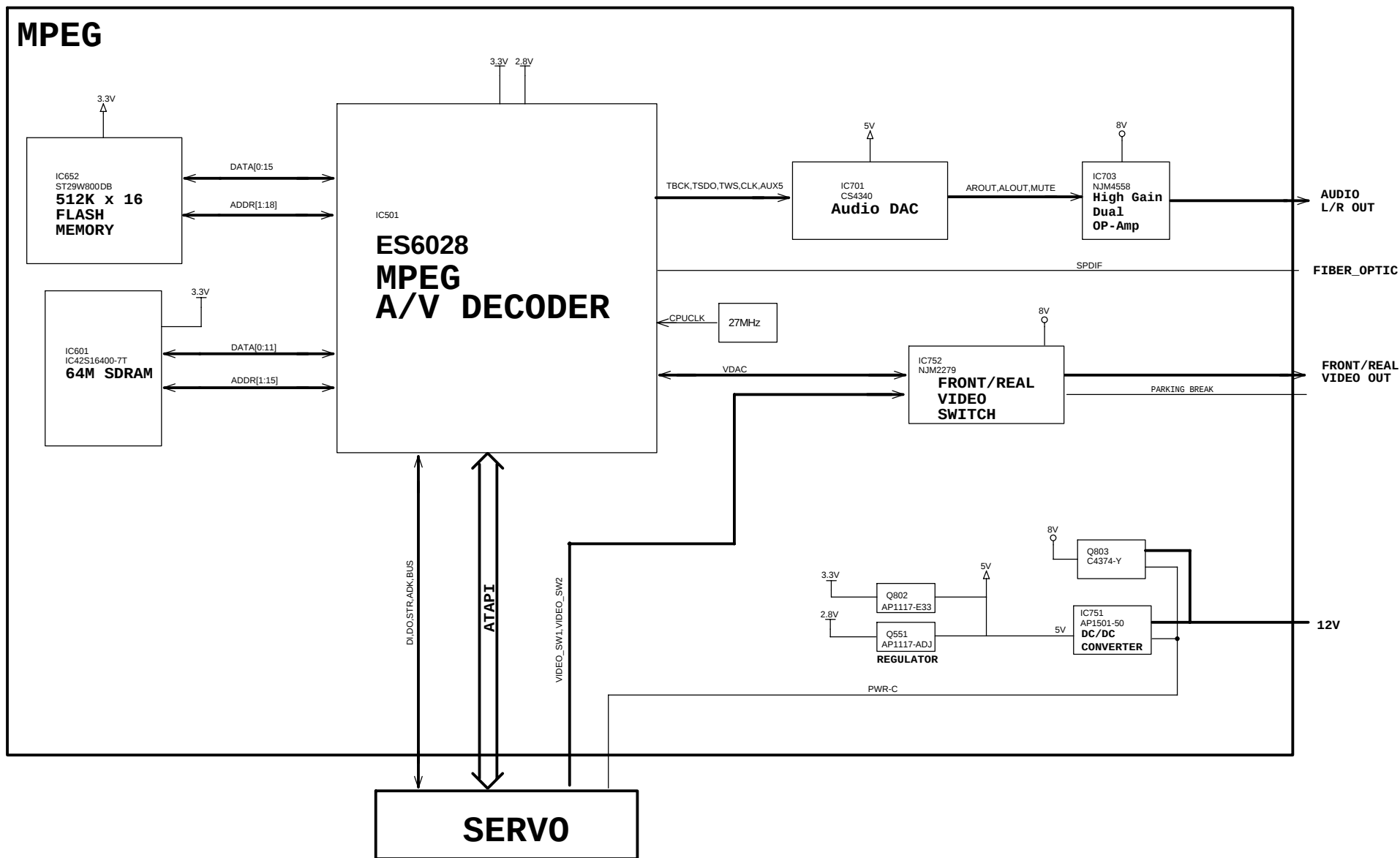




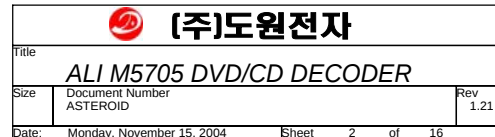


PROLOGY DVD-500HDF

ALI M5705 SERVO

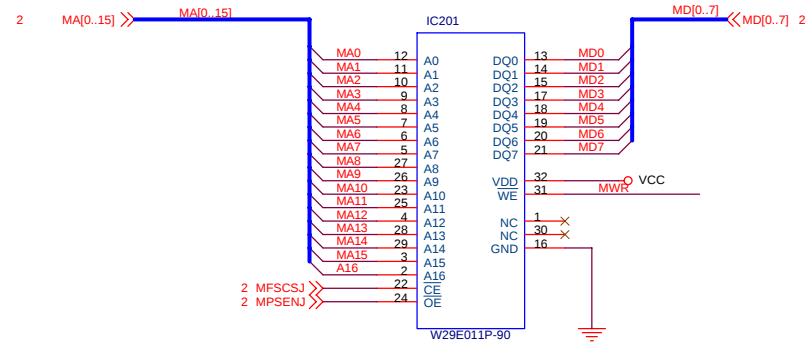
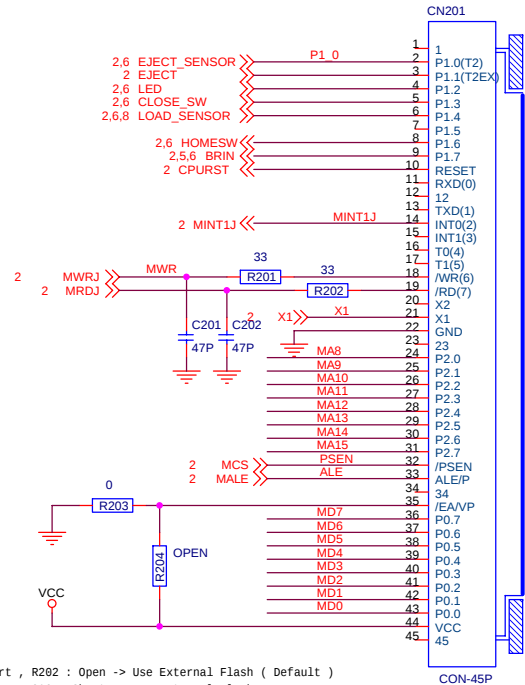
ESS ES6028 MPEG

Title		
<i>TITLE</i>		
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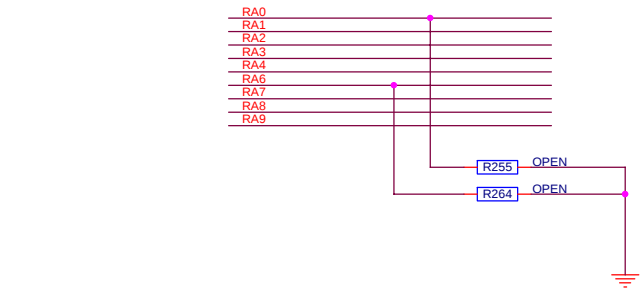
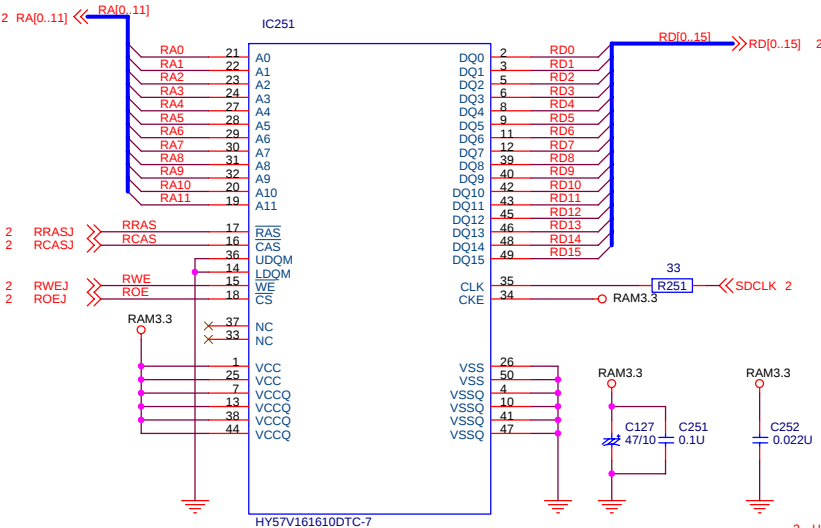
Use Internal uP Mount RN201,RN202 = 33 Both and Remove R204
Use External uP Remove RN201,RN202 and Mount R204 =1K (Default)

ALI 5705 EM CONNECTOR



Edit on 010927

If U4 = 64K Flash ROM Then Mount R62 = 0 & Remove R61 (Default)
If U4 = 128K Flash ROM Then Mount R61 = 0 & Remove R62



HARDWARE SETTING:

RA[9] : Flash ROM Type
1 : Now is using 64K Flash, so that XMP1[0] can be used as GPIO. (Default)
0 : Now is using 128K Flash, so that XMP1[0] is used as A16

RA[8] : MP Type
1 : MP type is Intel 8032.
0 : MP type is H8

RA[7] : Microprocessor programmable I/O port 1 pin control
1 : By internal microprocessor
0 : By registers to decided input/output

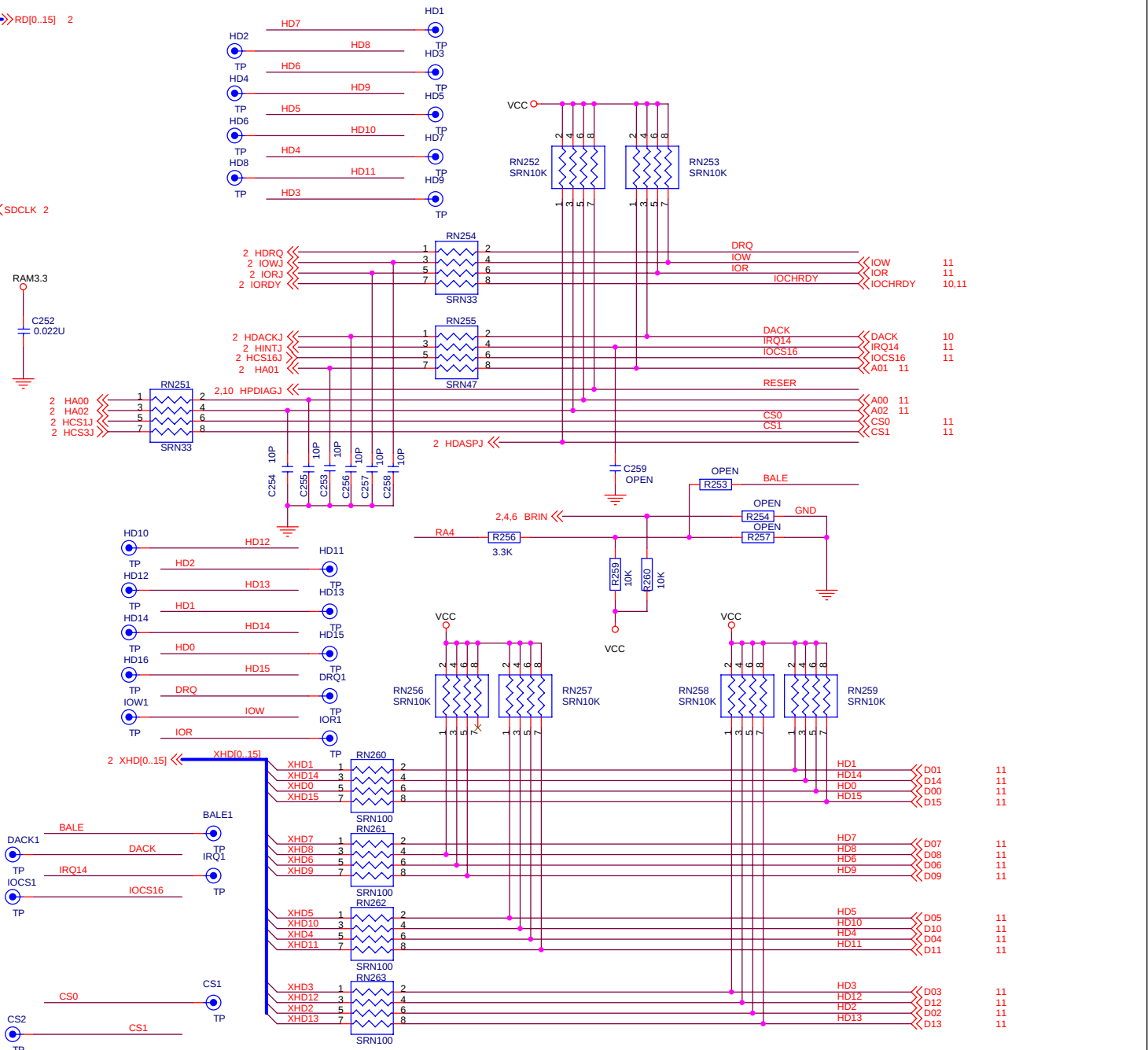
RA[6] : System testpin output
1 : Normal operation
0 : System test pin output

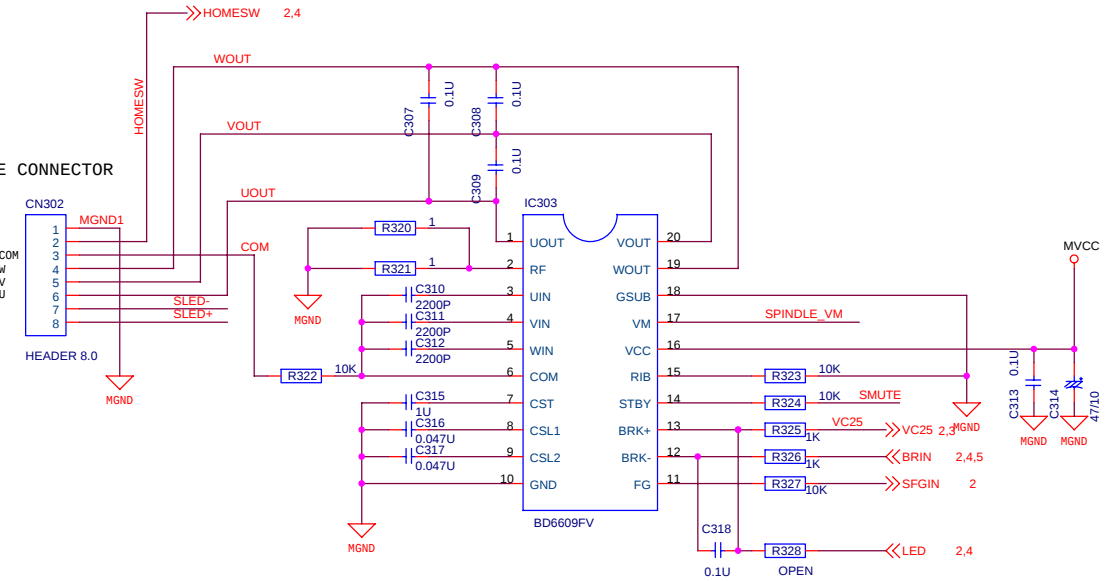
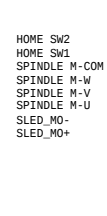
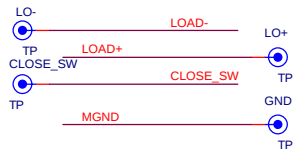
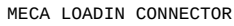
RA[4] : IDE Master/Slave
1 : Slave
0 : Master

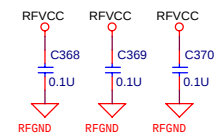
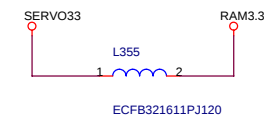
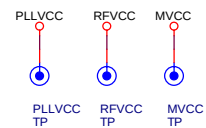
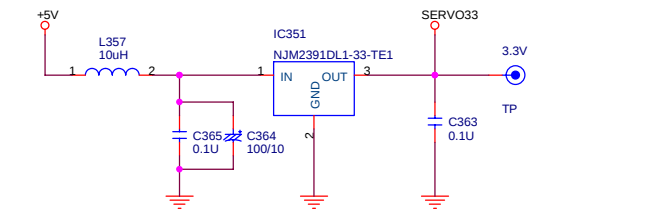
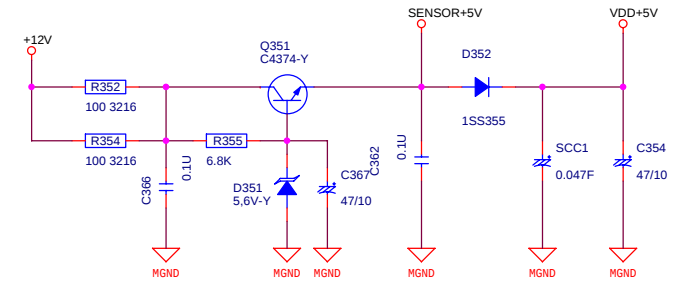
RA[3] : Microprocessor I/F pins configuration (For Testting Only)
1 : Normal mode
0 : M5701 compatible mode

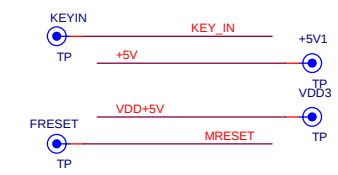
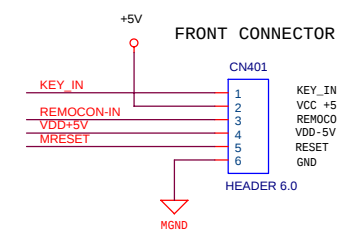
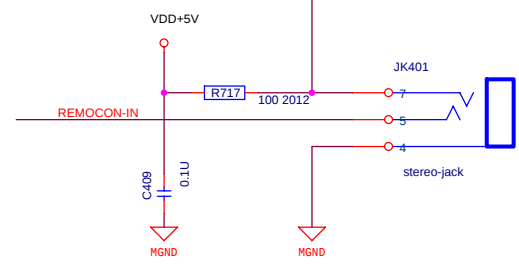
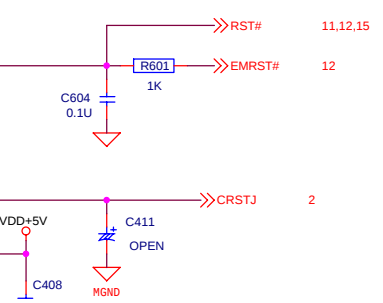
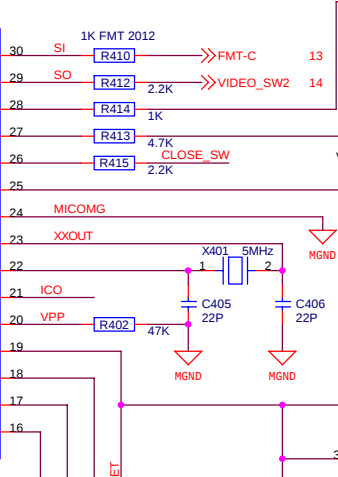
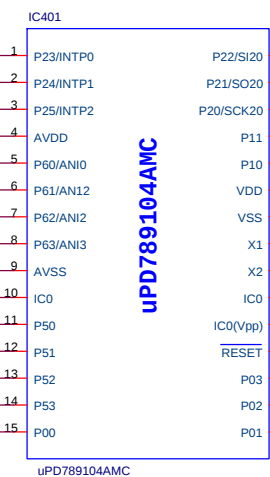
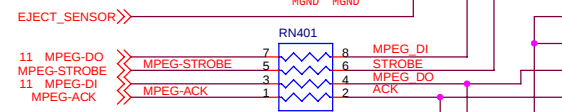
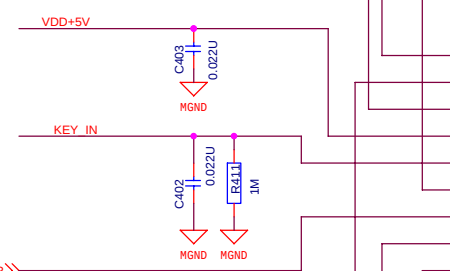
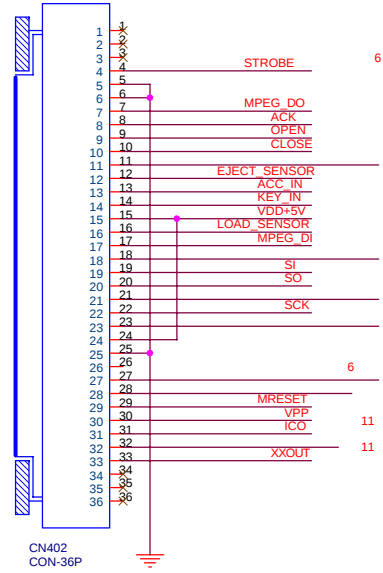
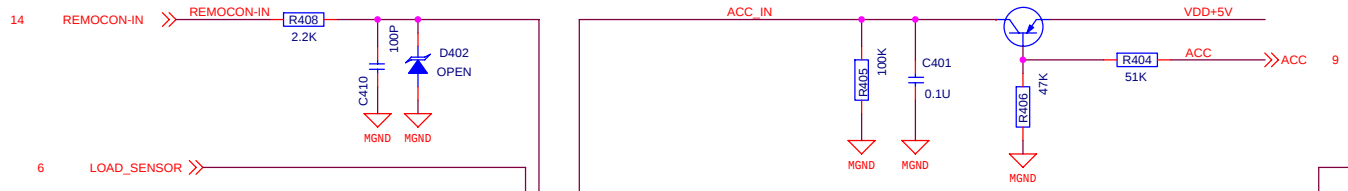
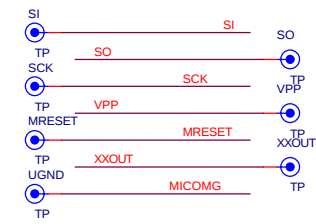
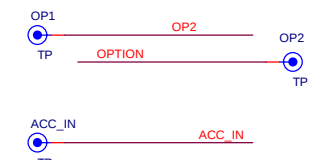
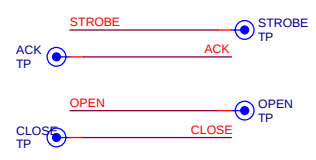
RA[2] : Clock source selection (For Testting Only)
1 : Select internal clock
0 : Select external clock

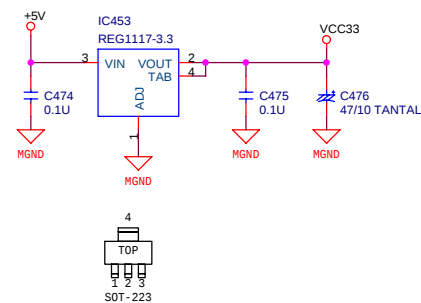
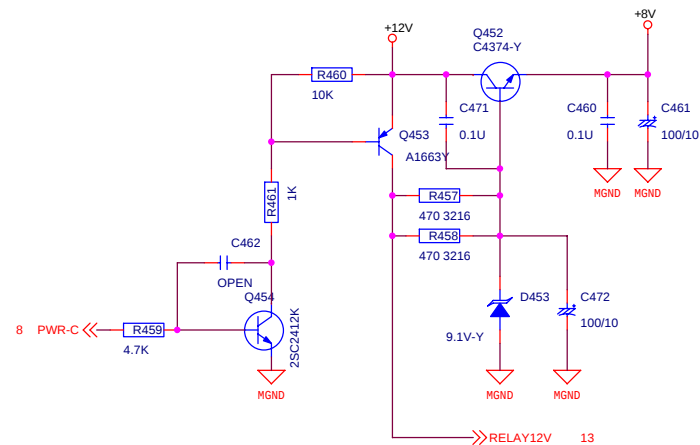
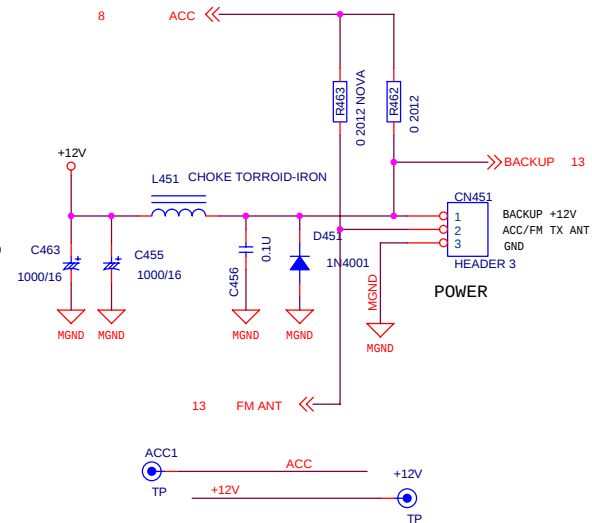
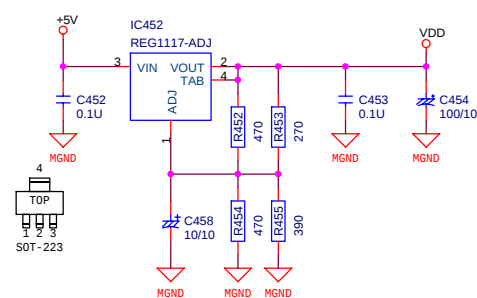
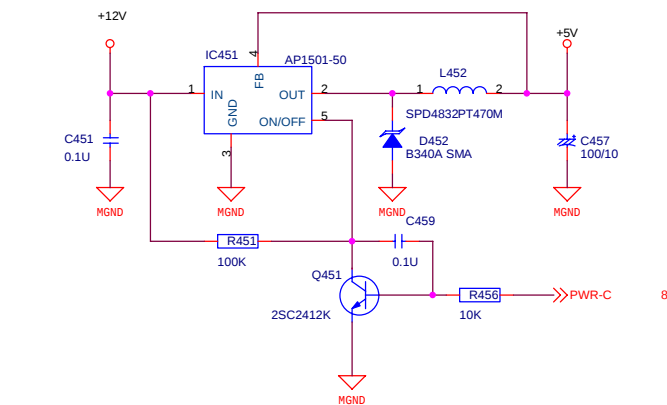
RA[1-0] : MCU Mode selection
11 : Normal Mode (internal uP, internal address latch)
10 : Output uP Mode (ICE Mode)
01 : Test mode for internal uP testing
00 : internal uP mode with external address latch

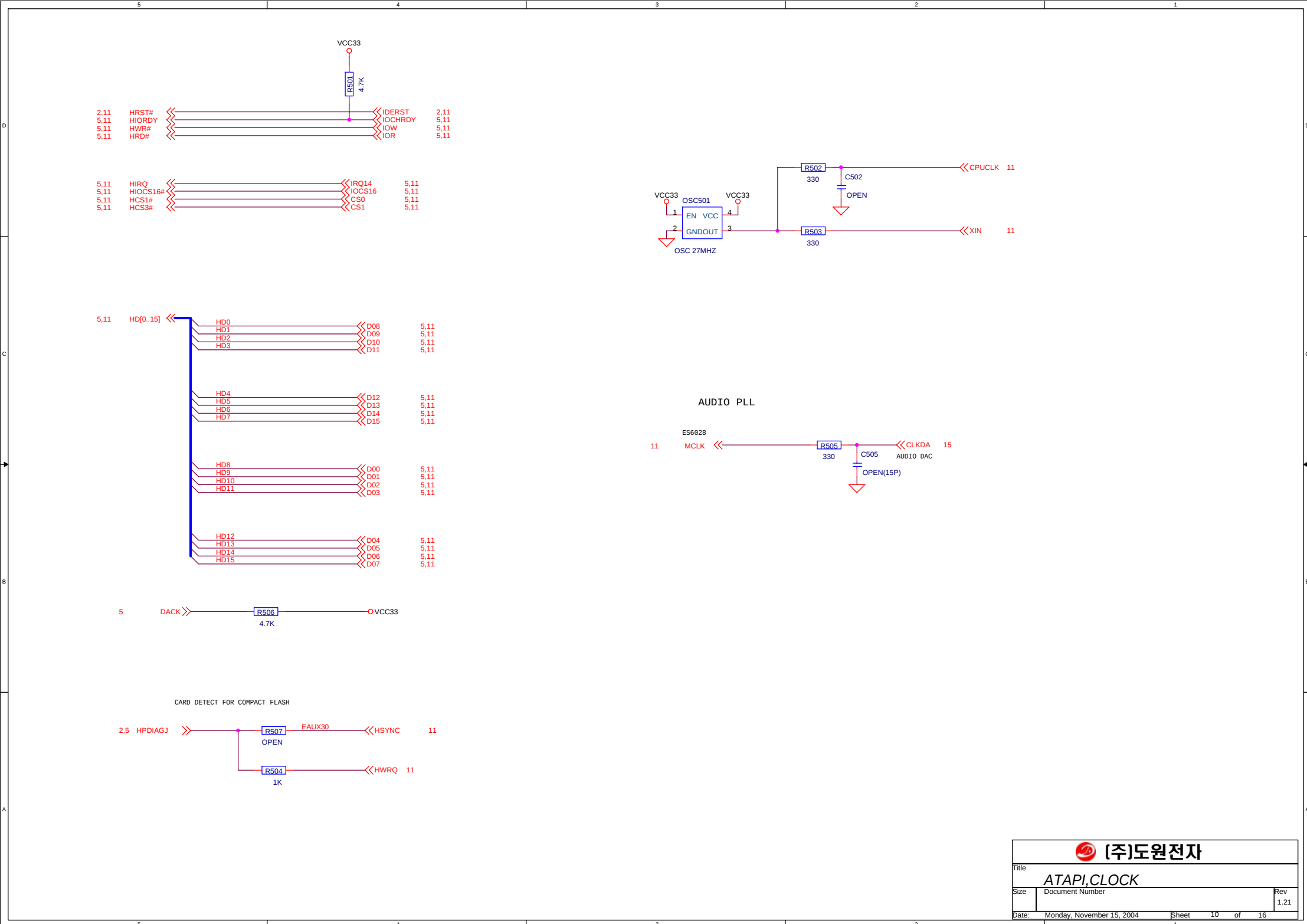


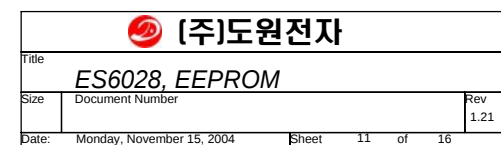


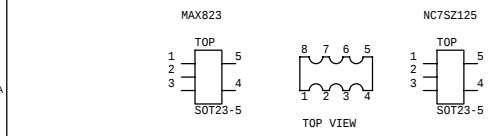
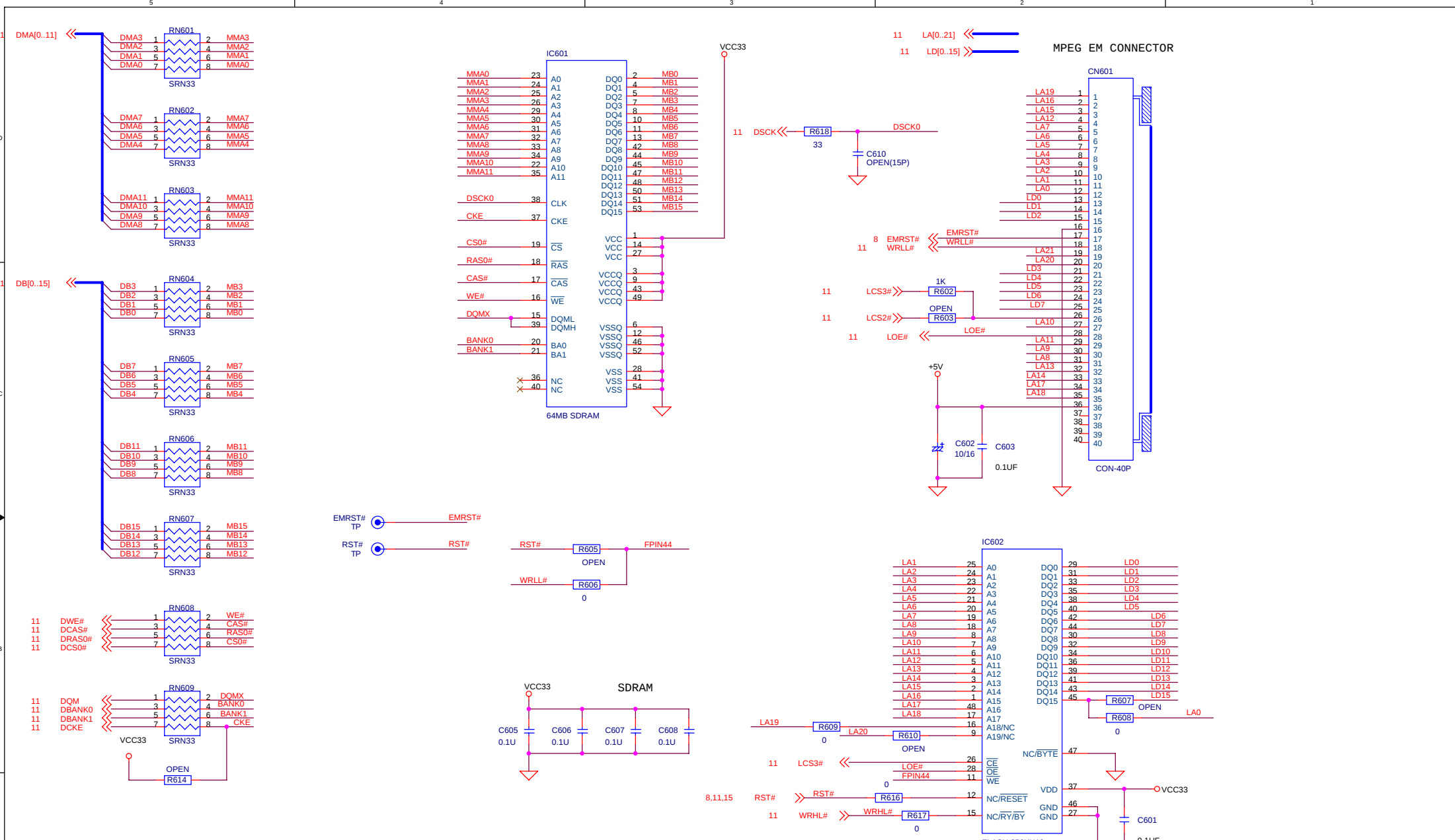












MANUFACTURE	PART #	PIN 1	PIN 2	PIN 43	PIN 44
MACRONIX	MX298100	WE#	A18	VCC/GND	WP#
MACRONIX	MX29F1610	WE#	A18	A19	WP#
INTEL	28F400	VPP	WP#	WE#	RP#
INTEL	28F800	VPP	A18	WE#	RP#
SST	SST39LF160	NC	A18	A19	WE#

**[주]도원전자**

TitleSDRAM,RESET,FLASH MEMORY

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Date:Monday, November 15, 2004Sheet12 of 16

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